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Version with Markings to Show Changes Made:

In the Claims:

Cancel claim 3.

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Claim 1 (twice amended). An integrated semiconductor circuit, comprising:

a plurality of pad cells to be monitored in one operating mode by a functional test, said plurality of pad cells each having a connecting pad, an upstream output driver, and a connection for an input signal;

a signal transmitter for producing periodic signal sequences, said signal transmitter having a connection for a periodic output signal connected to said [connection] connections for an input signal of said plurality of pad cells to be tested, in order to test a transmission response of said plurality of pad cells in said one operating mode; and

shift register cells each connected in series between said connection for an input signal of a respective one of said plurality of pad cells and said connection of said signal transmitter for an output signal;

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said connections for an input signal of said plurality of pad cells being connected in parallel to said connection of said signal transmitter for an output signal.

Claim 7 (amended). [The] An integrated semiconductor circuit [according to claim 1, wherein] , comprising:

a plurality of pad cells to be monitored in one operating mode by a functional test, said plurality of pad cells each having a connecting pad, an upstream output driver, and a connection for an input signal;

a signal transmitter for producing periodic signal sequences, said signal transmitter having [said signal transmitter contains] a clock-controlled bistable multivibrator of the Tflip-flop type and a connection for a periodic output signal connected to said connection for an input signal of said plurality of pad cells to be tested, in order to test a transmission response of said plurality of pad cells in said one operating mode; and

shift register cells each connected in series between said connection for an input signal of a respective one of said plurality of pad cells and said connection of said signal transmitter for an output signal.